





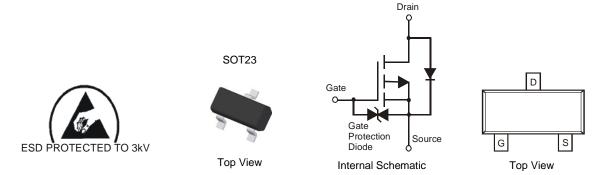
P-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected Up To 3KV**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.008 grams (approximate)



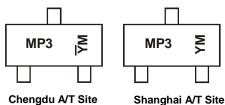
Ordering Information (Note 4)

Part Number	Part Number Compliance Case		Packaging
DMP2035U-7	Standard	SOT23	3000 / 7" Tape & Reel
DMP2035UQ-7	Automotive	SOT23	3000 / 7" Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



MP3 = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site) YM = Date Code Marking for CAT (Chengdu Assembly/ Test site)

Y or \overline{Y} = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Date Code Key

Year	2009	9	2010		2011	20	12	2013		2014	2	2015
Code	W		Χ		Υ	1	7	Α		В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	;		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	-20	V	
Gate-Source Voltage		V _{GSS}	±8	V	
Continuous Drain Current (Note 5) Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			I _D	-3.6 -2.9	А
Pulsed Drain Current (Note 6)			I _{DM}	-24	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Power Dissipation (Note 5)	P _D	0.81	W	
Thermal Resistance, Junction to Ambient @T _A = +25°C	R _{θJA}	153.5	°C/W	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C	

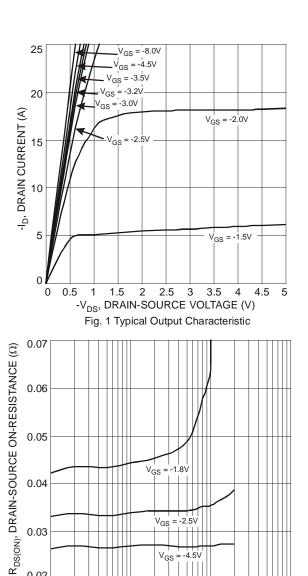
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	-1.0	μA	$V_{DS} = -20V$, $V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	-0.4	-0.7	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
			23 30 41	35 45 62		$V_{GS} = -4.5V, I_{D} = -4.0A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_			mΩ	$V_{GS} = -2.5V, I_D = -4.0A$	
						$V_{GS} = -1.8V, I_D = -2.0A$	
Forward Transfer Admittance	Y _{fs}	_	14	_	S	$V_{DS} = -5V, I_{D} = -4A$	
Diode Forward Voltage	V _{SD}	_	-0.7	-1.0	V	V _{GS} = 0V, I _S = -1A	
DYNAMIC CHARACTERISTICS (Note 8)		•	•		•		
Input Capacitance	C _{iss}	_	1610	_	pF		
Output Capacitance	Coss	_	157	_	pF	$V_{DS} = -10V, V_{GS} = 0V$ - f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	145	_	pF	-1 = 1.0IVID2	
Gate Resistance	Rg	_	9.45	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	15.4	_	nC	4.514.14	
Gate-Source Charge	Q _{gs}	_	2.5	_	nC	$V_{GS} = -4.5V, V_{DS} = -10V,$	
Gate-Drain Charge	Q_{gd}	_	3.3	_	nC	$I_D = -4A$	
Turn-On Delay Time	t _{D(on)}	_	16.8	_	ns		
Turn-On Rise Time	t _r	_	12.4	_	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$	
Turn-Off Delay Time	t _{D(off)}	_	94.1	_	ns	$R_L = 10\Omega, R_G = 6.0\Omega, I_D = -1A$	
Turn-Off Fall Time	t _f	_	42.4	_	ns		

Notes:

- 5. Device mounted on FR-4 PCB with 2 oz. Copper and test pulse width t \leq 10s.
- Repetitive rating, pulse width limited by junction temperature.
 Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.



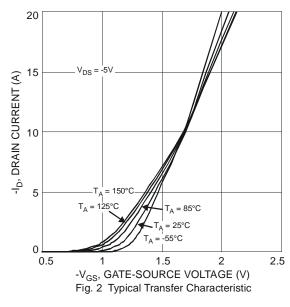


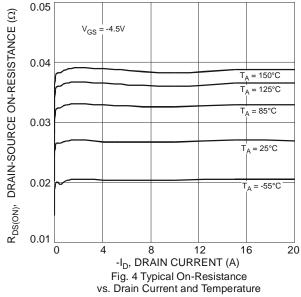
-4.5V V_{GS}

-I_D, DRAIN-SOURCE CURRENT (A) Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

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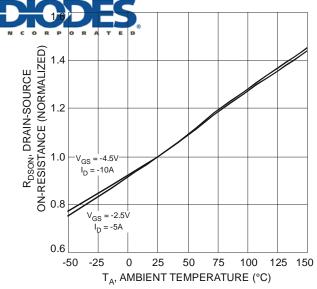
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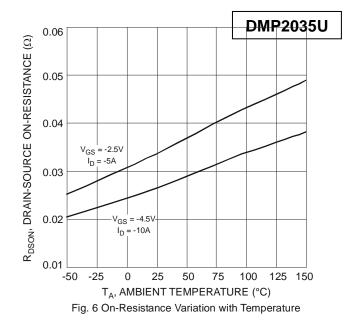




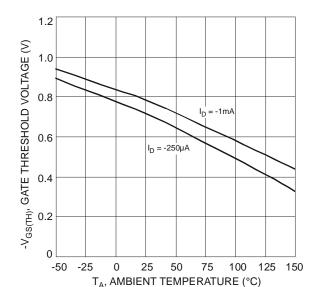
0.02

0.1









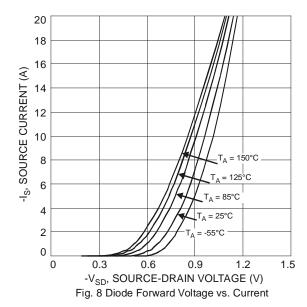
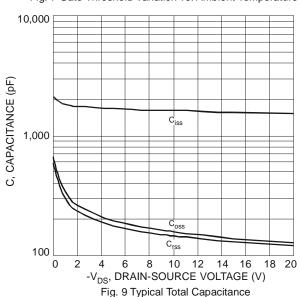
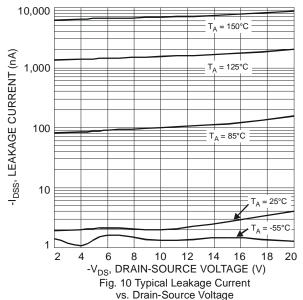
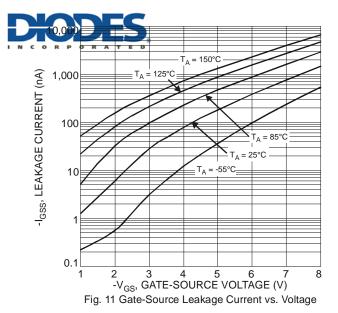
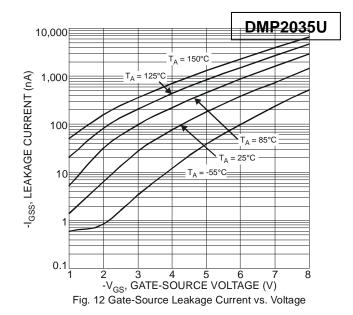


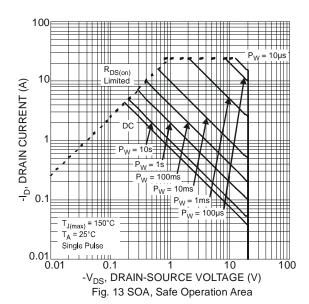
Fig. 7 Gate Threshold Variation vs. Ambient Temperature











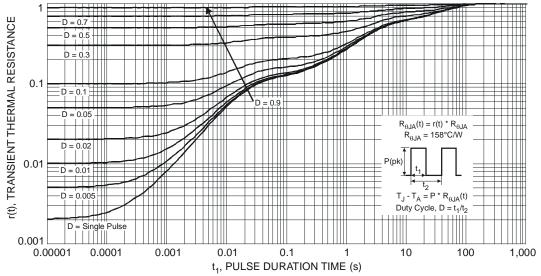
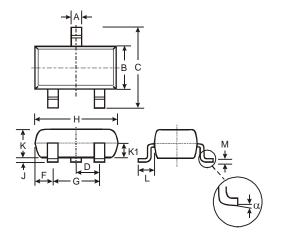


Fig. 14 Transient Thermal Response



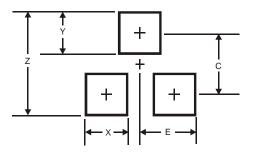
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT23							
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
K	0.903	1.10	1.00					
K1	-		0.400					
L	0.45	0.61	0.55					
М	0.085	0.18	0.11					
α	0°	8°	-					
All	All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35

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DMP2035U

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